

WHAT IS CLAIMED IS:

1. A delay line comprising:  
a dielectric substrate including a pair of main surfaces;  
a transmission line disposed on one of the main surfaces of the dielectric  
substrate;  
a ground conductor disposed on the other of the main surfaces of the  
dielectric substrate; and  
a capacitance being disposed on the dielectric substrate and connected to the  
transmission line for setting a desired delay time of the delay line.
- 5  
2. A delay line according to claim 1, wherein said capacitance is adjustable.
3. A delay line according to claim 2, wherein said capacitance is provided by  
a variable capacitor.
4. A delay line according to claim 2, wherein said capacitance is provided by  
a varicap diode.
5. A delay line according to claim 1, wherein said capacitance is provided by  
a diode.
6. A delay line according to claim 5, wherein said diode is a varicap diode.
7. A delay line according to claim 1, wherein said capacitance is connected  
in parallel to the transmission line.
8. A delay line comprising:  
a multilayer structure formed by laminating a plurality of dielectric layers;

a transmission line formed on a dielectric layer embedded in the multilayer structure;

5 a plurality of ground conductors disposed on the dielectric layers and a pair of said ground conductors being disposed on opposite sides of the transmission line; and

a capacitance disposed on the multilayer structure and connected to the transmission line for setting a desired delay time of the delay line.

9. A delay line according to claim 8, wherein said capacitance is adjustable.

10. A delay line according to claim 9, wherein said capacitance is provided by electrodes formed on respective ones of said dielectric layers.

11. A delay line according to claim 9, wherein said capacitance is provided by a variable capacitor.

12. A delay line according to claim 9, wherein said capacitance is provided by a varicap diode.

13. A delay line according to claim 8, wherein said capacitance is provided by a diode.

14. A delay line according to claim 13, wherein said diode is a varicap diode.

15. A delay line according to claim 8, wherein said capacitance is connected in parallel to the transmission line.

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